L	Hits	Search Text	DB	Time stamp
Number 10	104	712/236.ccls.	USPAT;	2004/03/17
		. 12, 200, 0025	IBM TDB	22:33
11	107	712/235.ccls.	USPĀT;	2004/03/17
12	200	712/220	IBM_TDB	22:33
12	208	712/229.ccls.	USPAT; IBM TDB	2004/03/17 22:33
16	6	(predicate near4 register) same switch	USPAT;	2004/03/17
			EPO; JPO;	23:01
			IBM_TDB	
17	43	"switch instruction" same register same (status state valid\$4)	USPAT;	2004/03/17
18	67	(status state valid34) "switch instruction" same register same	EPO; JPO USPAT;	2004/03/17
		(predicate one bit)	EPO; JPO	23:01
19	34	((multi-way multiway) with branch) same	USPAT;	2004/03/17
20	144	(status state valid\$4)	EPO; JPO	23:02
20	144	predicate near4 register	USPAT; EPO; JPO;	2004/03/17 23:02
		r	IBM TDB	25.02
21	62	("switch instruction") same register same	USPAT;	2004/03/17
		address	EPO; JPO	23:02
22	238	(switch near3 instruction) same (register with address)	USPAT;	2004/03/17
23	5	(continuous contiguous succeeding	EPO; JPO USPAT;	23:02
		sequential) same (predicat\$4 with	EPO; JPO	23:02
		register)	-	
24	48	((multi-way multiway) with branch) same	USPAT;	2004/03/17
<u>_</u> .	207	register (switch near3 instruction) same (register	EPO; JPO USPAT	23:02
_	207	with address	USPAI	2004/03/17 . 23:02
-	2	("switch instruction") same address	IBM TDB	2003/09/03
			-	22:26
-	2	("switch instruction") same register same	IBM_TDB	2003/09/02
_	51	address ("switch instruction") same register same	USPAT	17:23 2004/03/17
	31	address	OSPAI	23:02
74 I I I	5	("switch instruction")	IBM_TDB	2003/09/04
		710 (006 1		11:03
-	0	712/236.ccls.	IBM_TDB	2003/09/04
_	25	712/236.ccls. and switch	USPAT;	2003/09/08
d		,	IBM TDB	03:48
-	1	java near2 tableswitch	USPAT;	2003/09/04
	106	prodicate poard register	IBM_TDB USPAT;	13:37
	100	predicate near4 register	IBM TDB	2004/03/17
-	0	(predicate near4 register) with switch	USPAT;	2003/09/04
			IBM_TDB	17:43
-	18	(predicate near4 register) with branch	USPAT;	2003/09/04
_	7	712/236.ccls. and predicate	IBM_TDB USPAT;	13:40 2003/09/04
	11	.12, 200. Cold. and predicate	IBM TDB	17:27
-	5	(predicate near4 register) same switch	USPAT;	2004/03/17
			IBM_TDB	23:01
-	0	<pre>(predicate near4 register) and "switch instruction"</pre>	USPAT; IBM TDB	2003/09/07 22:30
_	530	"switch instruction"	USPAT	2003/09/07
				22:58
111	100	"switch instruction" same register	USPAT	2003/09/07
_	E 0	"avitab inatmustical" same mariatar	HCDAM	22:58
-	58	"switch instruction" same register same (predicate one bit)	USPAT	2004/03/17
_	39	"switch instruction" same register same	USPAT	2004/03/17
		(status state valid\$4)		23:01
-	66	, ,	USPAT;	2003/09/07
_	37	<pre>state valid\$4)) (switch near3 instruction) same (branch</pre>	IBM_TDB USPAT	23:01 2003/09/08
	' '	with address)	OSFAI	03:47
-	0	((multi-way multiway) with branch) same	USPAT	2003/09/10
L		register same (predicate)		15:59

_	42	((multi-way multiway) with branch) same	USPAT	2004/03/17
		register		23:02
-	3	((multi-way multiway) with branch) same	USPAT	2003/09/08
		predicat\$4		08:23
-	25	((multi-way multiway) with branch) same	USPAT	2004/03/17
		(status state valid\$4)		23:01
-	1	6571385.pn.	USPAT	2003/09/08
				08:46
-	529	(continuous contiguous succeeding	USPAT	2003/09/10
		sequential) same (predicat\$4)		16:00
-	4	(continuous contiguous succeeding	USPAT	2004/03/17
		sequential) same (predicat\$4 with		23:02
		register)		